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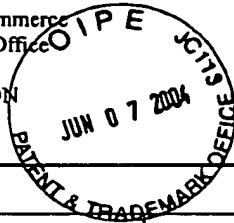
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INFORMATION DISCLOSURE CITATION
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Applicant: Akito Hara

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U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
CN	2002/0031876	Mar. 14, 2002	Hara et al.	438	166	8-22-01

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
CN	10-173192	Jun. 26, 1998	Japan			Abs. only	
CN	2002-033481	Jan. 31, 2002	Japan			Abs. only	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Makihira et al.; Double-Gate Poly-Si Thin-Film Transistors Fabricated Using Self-Aligned Technology; Am-LCD '01; Digest of Technical Papers; July 11-13, 2001

Examiner

Date Considered

2-16-05

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Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.